Lab 3 Descriptions

1. This program implements an RS latch. I did not use the code given in the lab manual because every time I tried to run a simulation on it, the code would fall into an infinite loop and would create an iteration limit reached error in ModelSim.
2. This program is a D-latch that uses SW[0] as the D input, and SW[1] as the clock. LEDR is the output of the latch.
3. This program uses two D-latches that use SW[0] as the D input, and SW[1] as the clock. The latches are in a master-slave setup.
4. This program uses a D-latch, positive edge triggered D flip flop, and a negative edge triggered D flip flop. The file Lab34Waveform.vwf does produce the same waveform as in the lab manual.
5. This lab inputs the number A from the 16 switches, stores it, and displays it on HEX[7:4]. After toggling SW[17] high, A is displayed and stored. B is inputted, stored on SW[17] going low, and displayed on HEX[3:0].